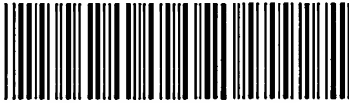


**Search Notes**

Application/Control No.

10/692,388

Examiner

Wai-Sing Louie

Applicant(s)/Patent under  
Reexamination

CHEN ET AL.

Art Unit

2814

**SEARCHED**

Class	Subclass	Date	Examiner
438	299-303, 197,585, 595	4/6/2005	WSL
257	344, 408	4/6/2005	WSL

**INTERFERENCE SEARCHED**

Class	Subclass	Date	Examiner
438/197,303,585,595		4/7/2005	WSL

**SEARCH NOTES  
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
FET, MOSFET, gate structure, composite sidewall spacer, Low dielectric constant material, nitride spacer	4/6/2005	WSL
Consulted with Steven Loke (Au-2811)	4/7/2005	WSL
Consulted with Phat Cao (Au 2814)	4/7/2005	WSL